Encontro Nacional de Sistemas Distribuídos

PAIO: General, Portable I/O Optimizations with Minor Application Modifications

20th USENIX Conference in File and Storage Technologies (USENIX FAST 2022)

Ricardo Macedo INESC TEC & University of Minho

Data-centric systems

- Data-centric systems have become an integral part of modern I/O stacks
- Good performance for these systems often requires storage optimizations
 - <u>Scheduling</u>, <u>caching</u>, <u>tiering</u>, <u>replication</u>, ...
- Optimizations are implemented in sub-optimal manner



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Data-centric systems

- Data-centric systems have become an integral part of modern I/O stacks
- Good per optimiz Sche

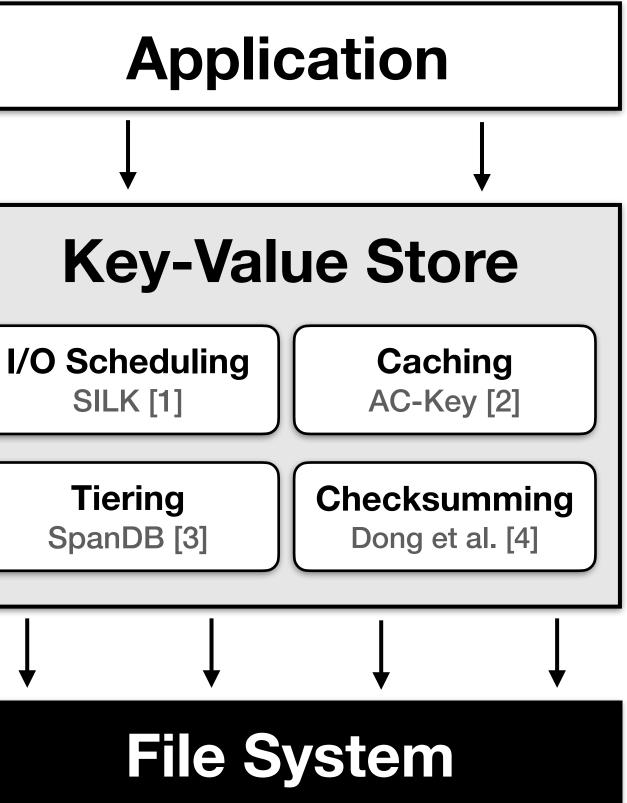




Solution Number 2018 Tightly coupled optimizations

- I/O optimizations are single purposed
- Require deep understanding of the system's internal operation model
- Require profound system refactoring
- Limited portability across systems

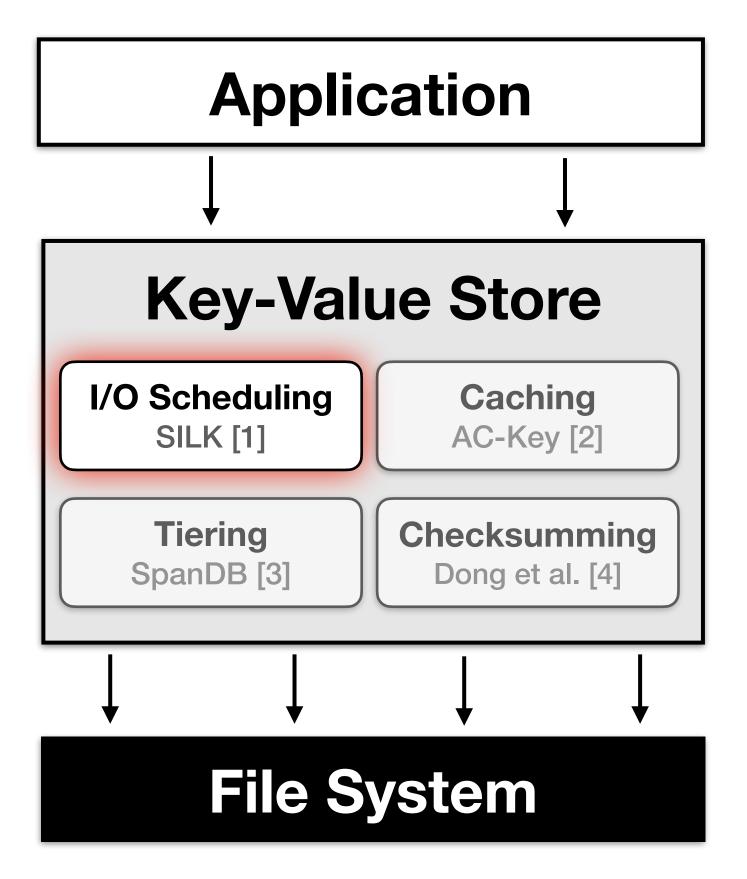
"SILK: Preventing Latency Spikes in Log-Structured Merge Key-Value Stores". Balmau et al. USENIX ATC 2019.
 "AC-Key: Adaptive Caching for LSM-based Key-Value Stores". Wu et al. USENIX ATC 2020.
 "SpanDB: A Fast, Cost-Effective LSM-tree Based KV Store on Hybrid Storage". Chen et al. USENIX FAST 2021.
 "Evolution of Development Priorities in Key-Value Stores Serving Large-scale Applications: The RocksDB Experience". Dong et al. USENIX FAST 2021.





Tightly coupled optimizations

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SILK's I/O Scheduler

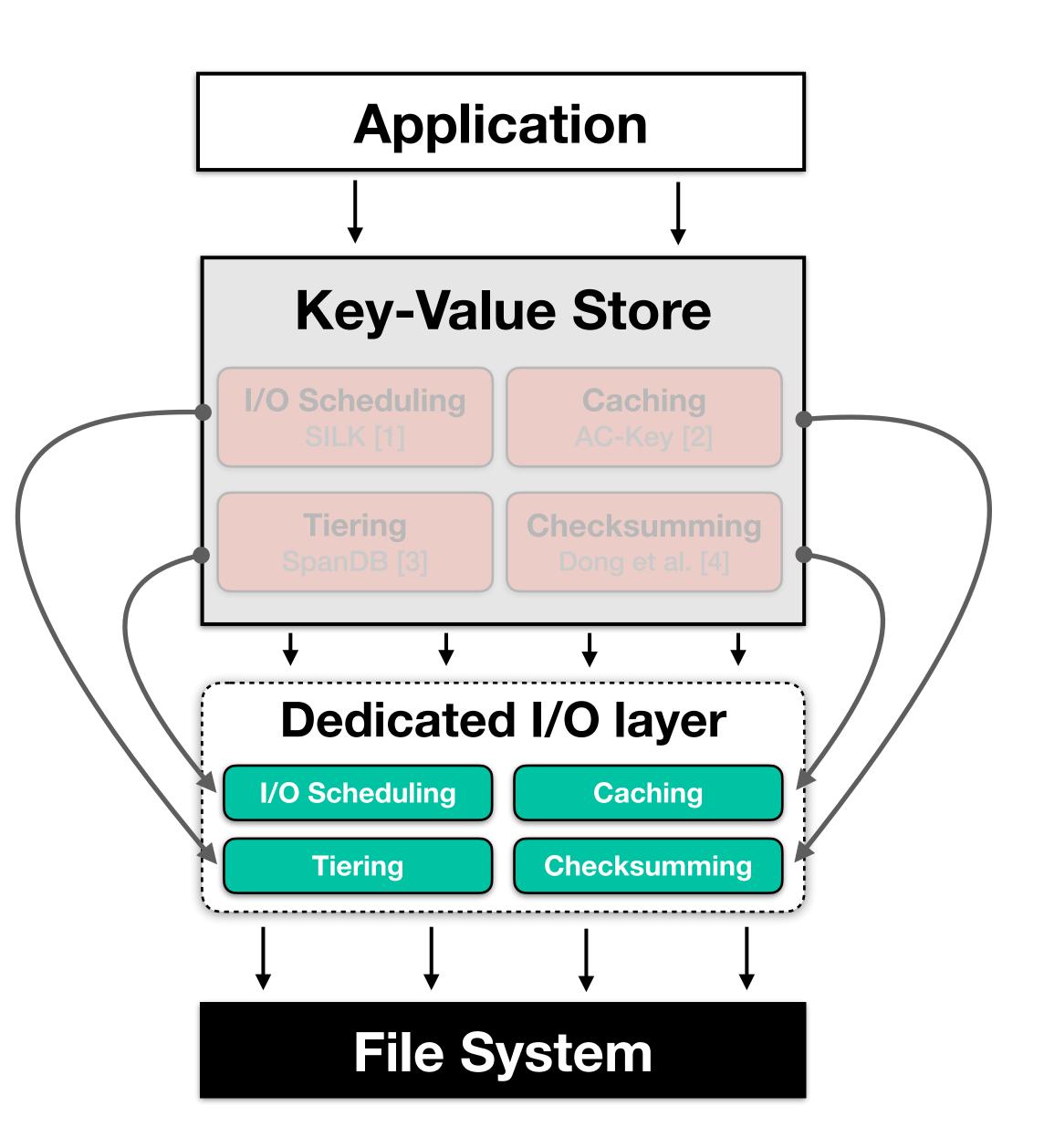
- Reduce tail latency spikes in RocksDB
- Controls the interference between foreground and background tasks
- Required changing several modules, such as *background operation handlers, internal queuing logic,* and *thread pools*

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Decoupled optimizations

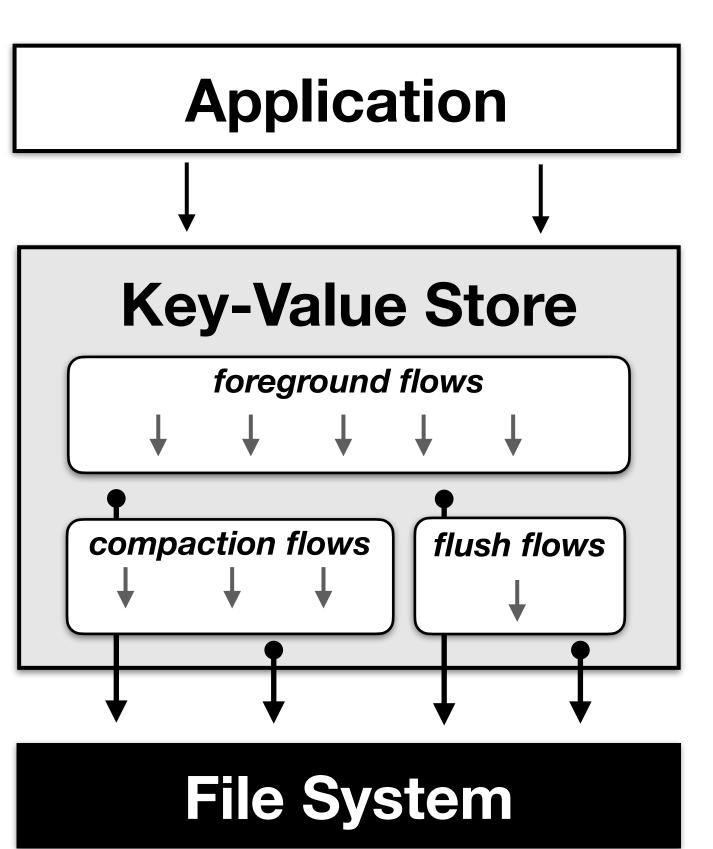
- I/O optimizations should be disaggregated from the internal logic
- Moved to a dedicated I/O layer
- Generally applicable
- Portable across different scenarios







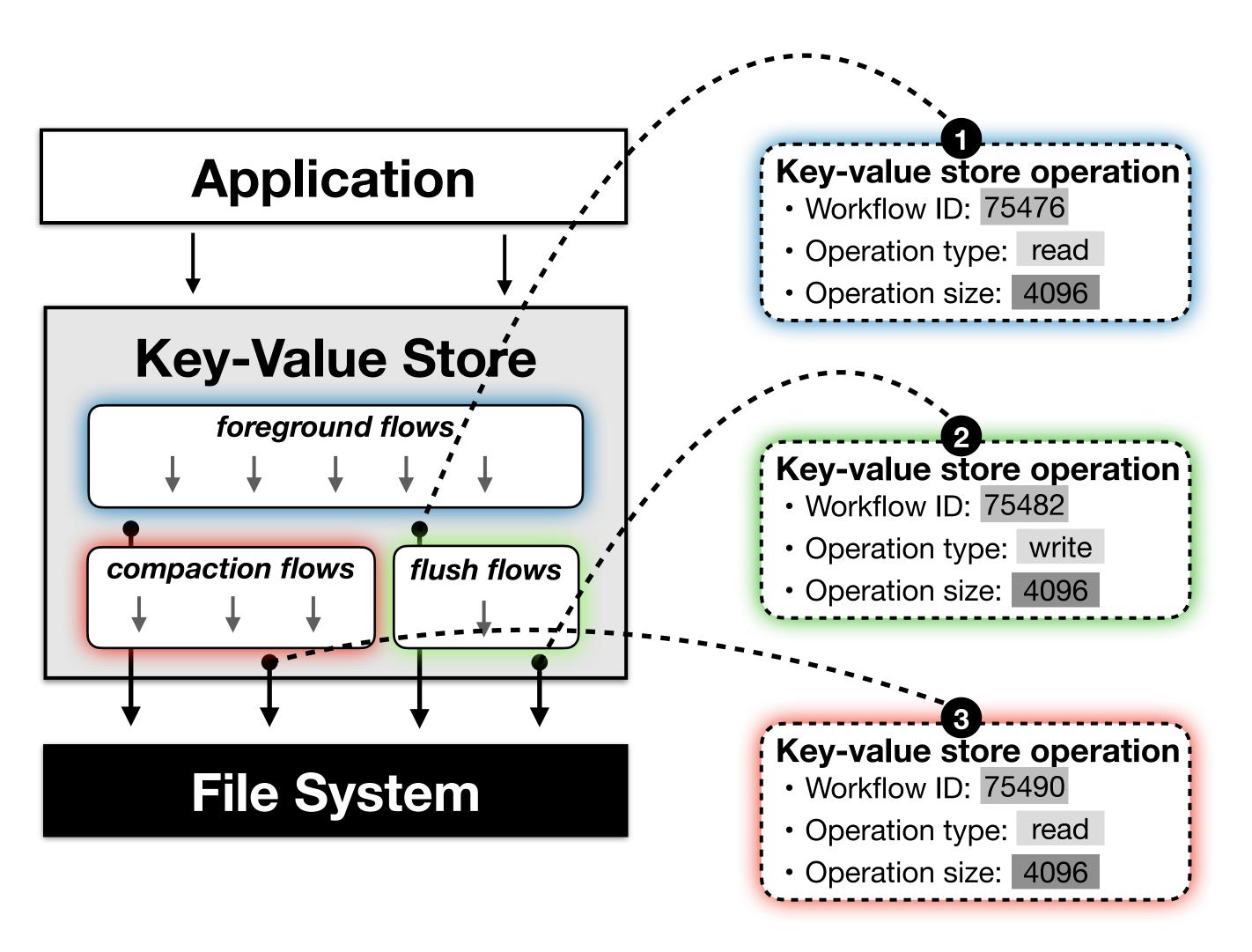
- Decoupled optimizations lose granularity and internal application knowledge
- I/O layers communicate through rigid interfaces
- Discard information that could be used to classify and differentiate requests





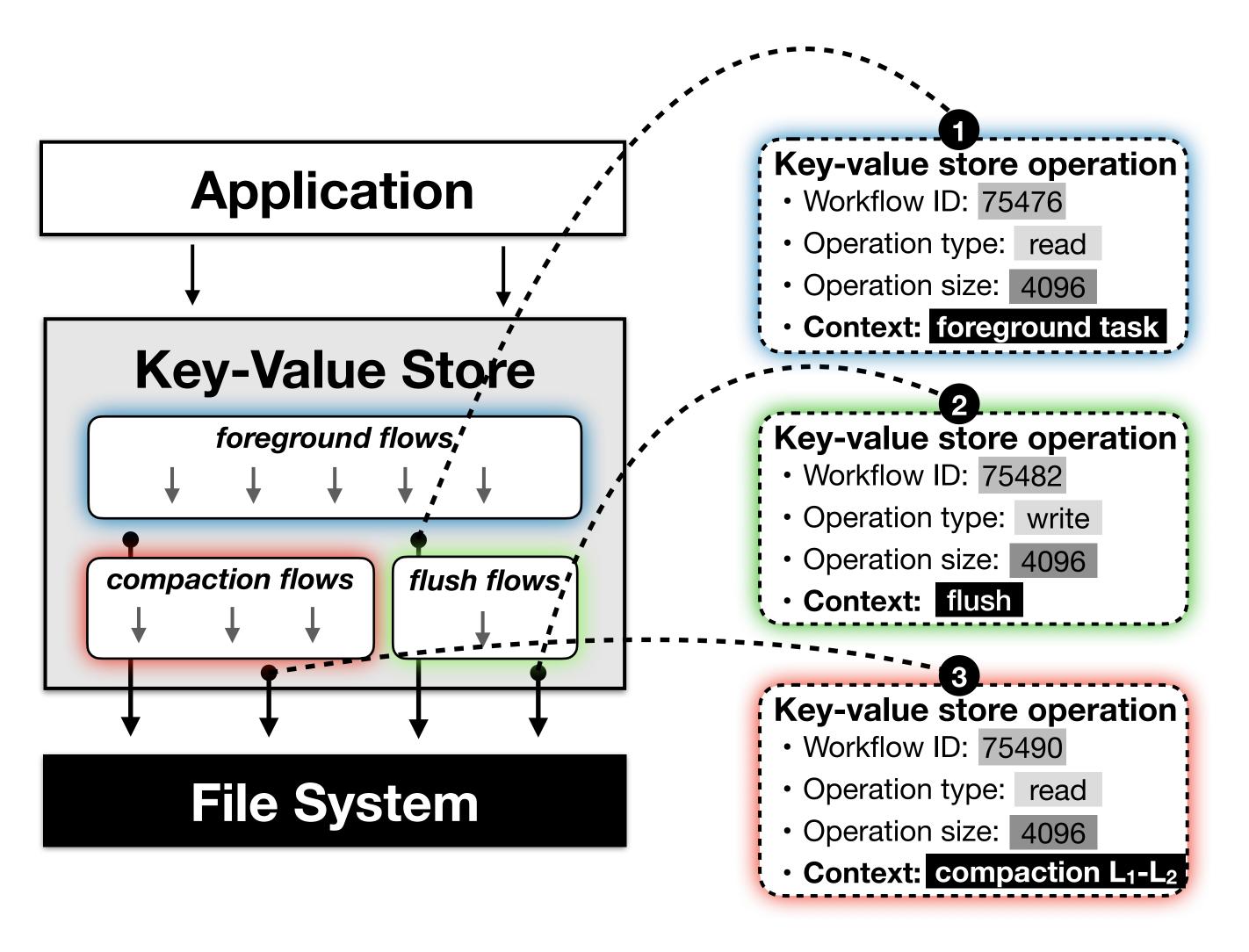


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Information propagation

- Application-level information must be propagated throughout layers
- Decoupled optimizations can provide the same level of control and performance



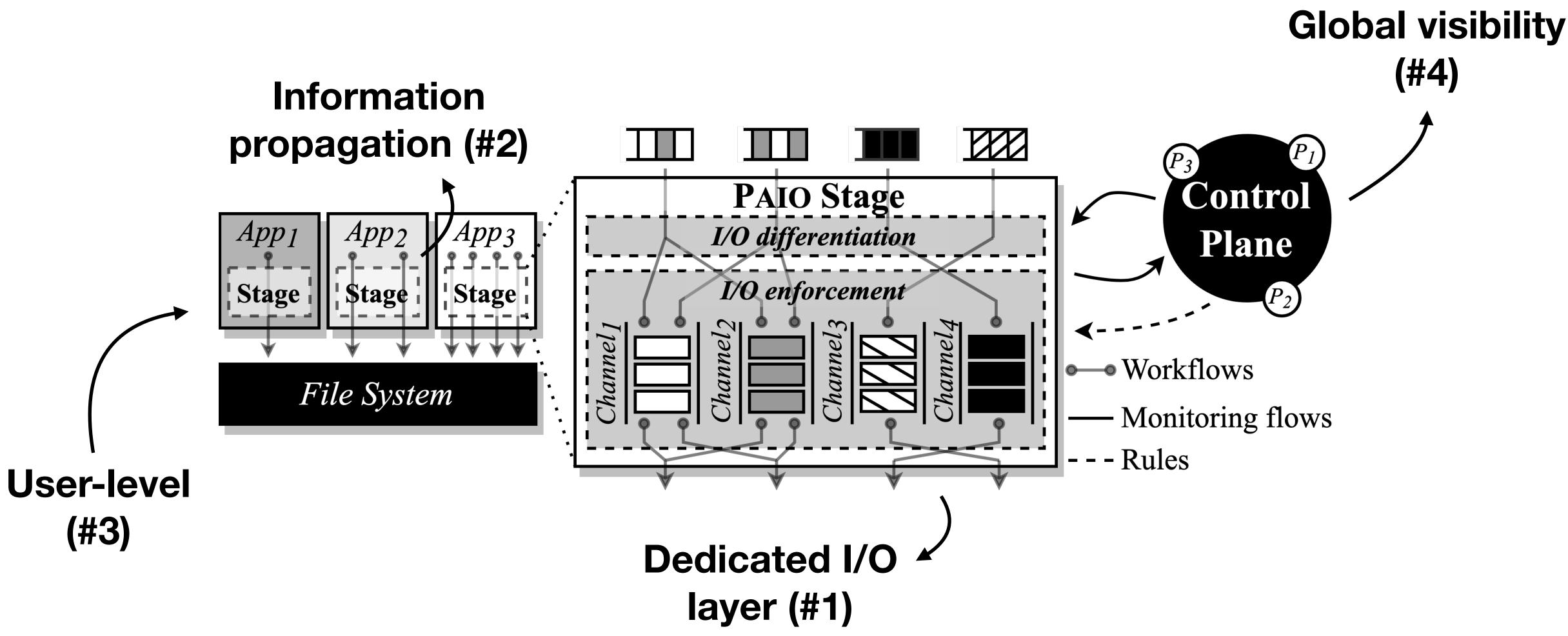
ΡΑΙΟ

- User-level framework for building portable and generally applicable optimizations \bullet
- Adopts ideas from **Software-Defined Storage** [6] \bullet
 - I/O optimizations are implemented outside applications as data plane stages
 - Stages are controlled through a control plane for coordinated access to resources
- Enables the propagation of application-level information through context propagation
- Porting I/O layers to use PAIO requires none to minor code changes

[5] "PAIO: General, Portable I/O Optimizations with Minor Application Modifications". Macedo et al. USENIX FAST 2022. [6] "A Survey and Classification of Software-Defined Storage Systems". Macedo et al. ACM CSUR 2020.

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PAIO design

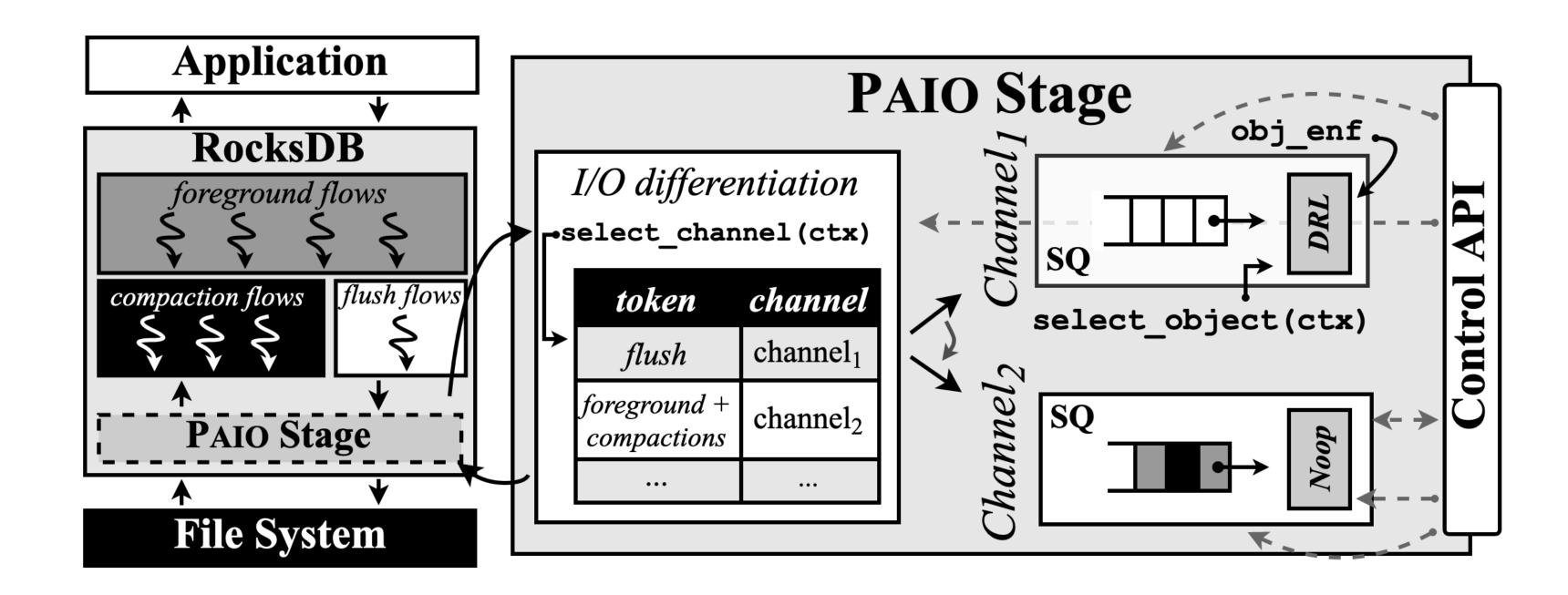




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PAIO design

- I/O differentiation
- I/O enforcement
- Control plane interaction



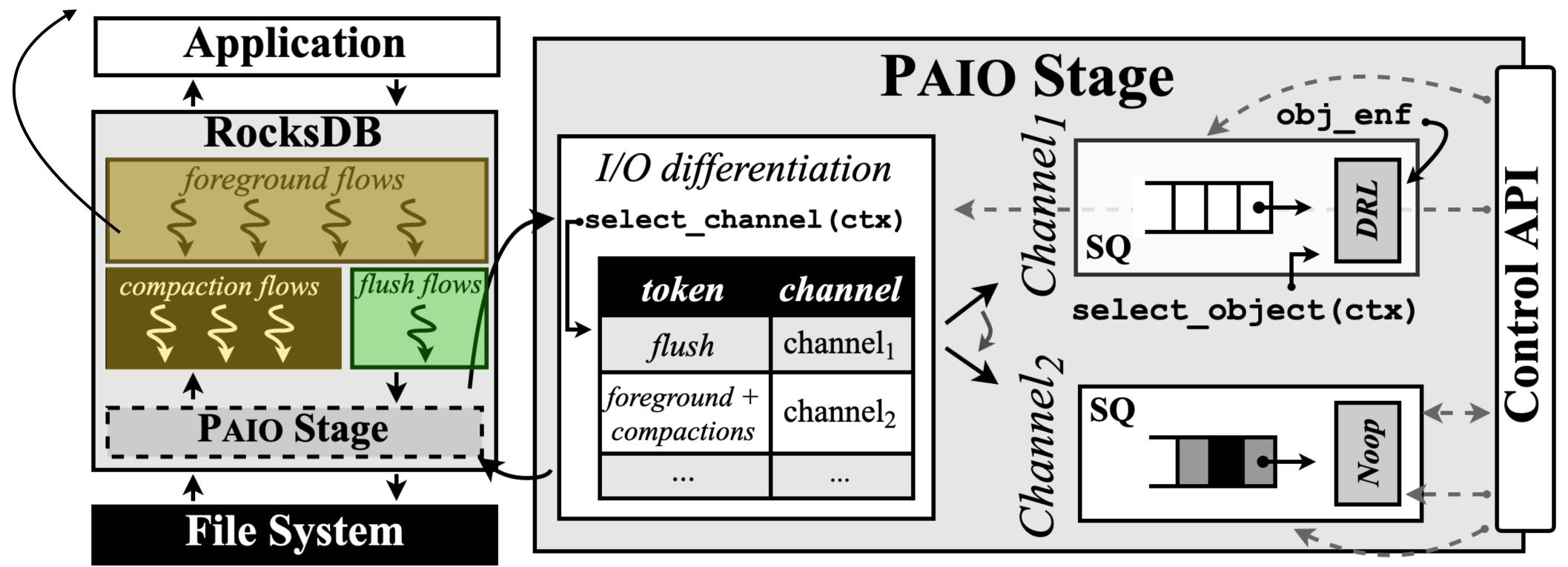
Policy: *limit the rate of RocksDB's flush operations to X MiB/s*



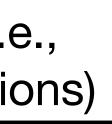
I/O differentiation

Context propagation:

instrumentation + propagation phases



Identify the origin of POSIX operations (i.e., foreground, compaction, or flush operations)

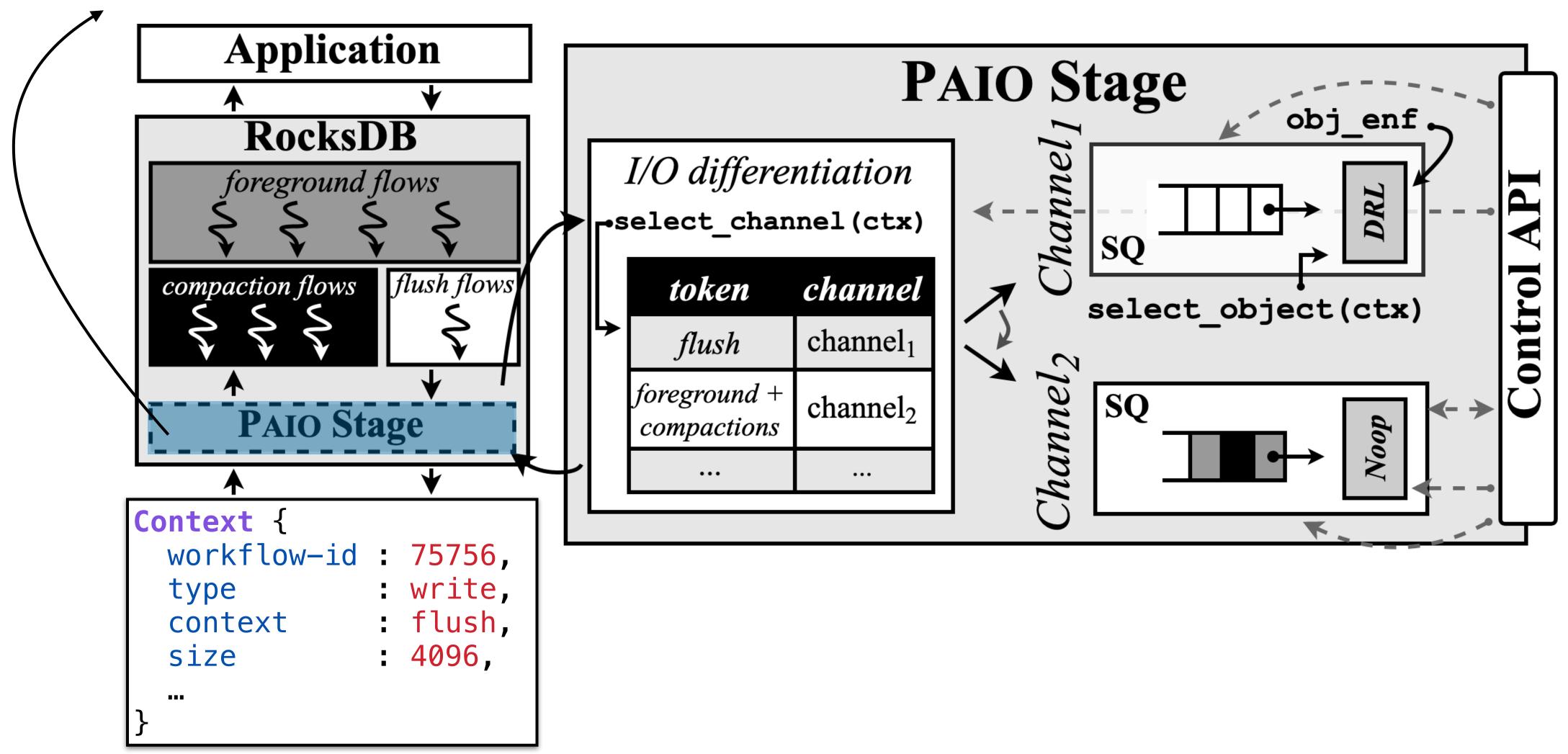




I/O differentiation

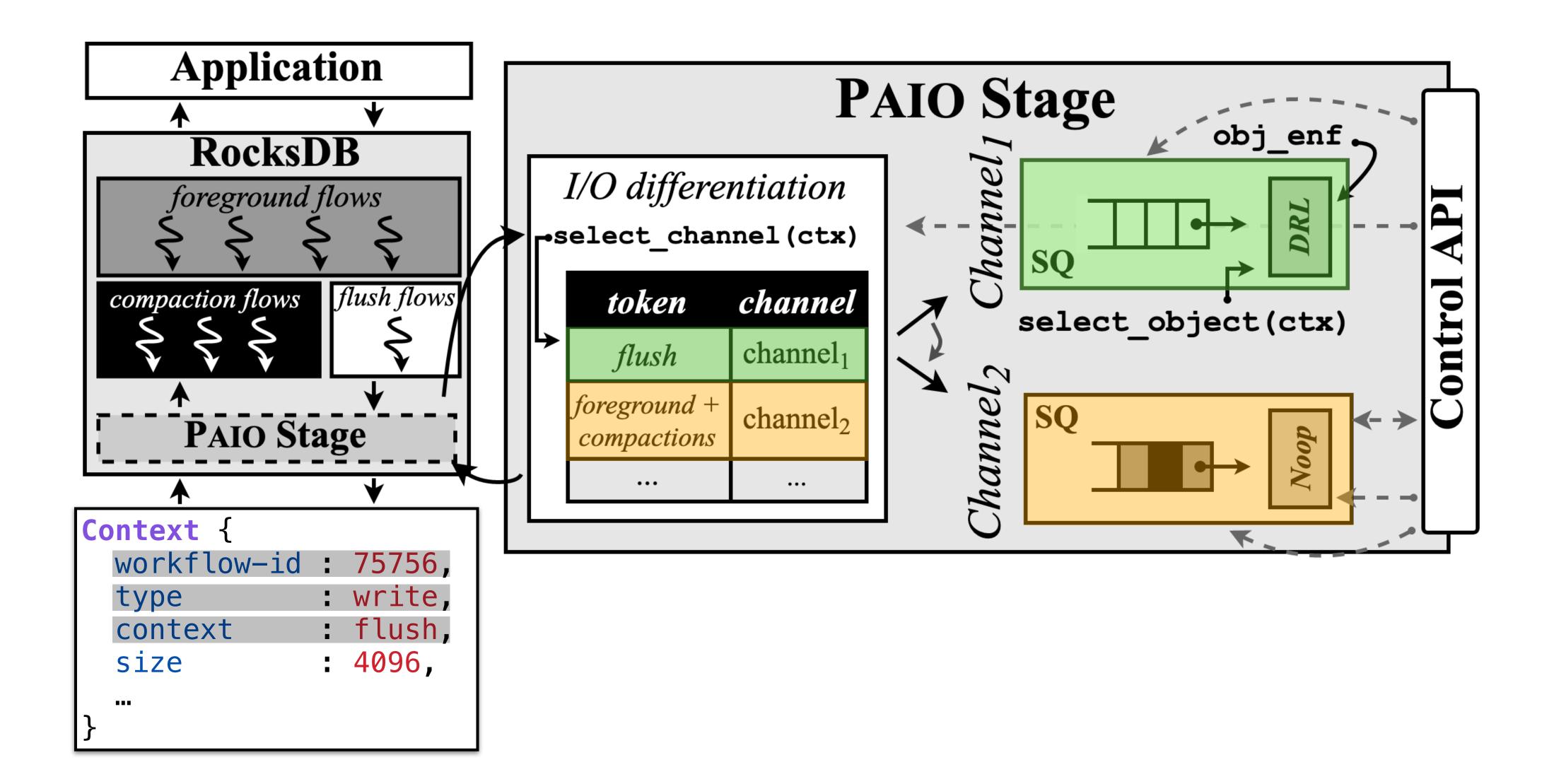
Context propagation:

propagation + classification phases



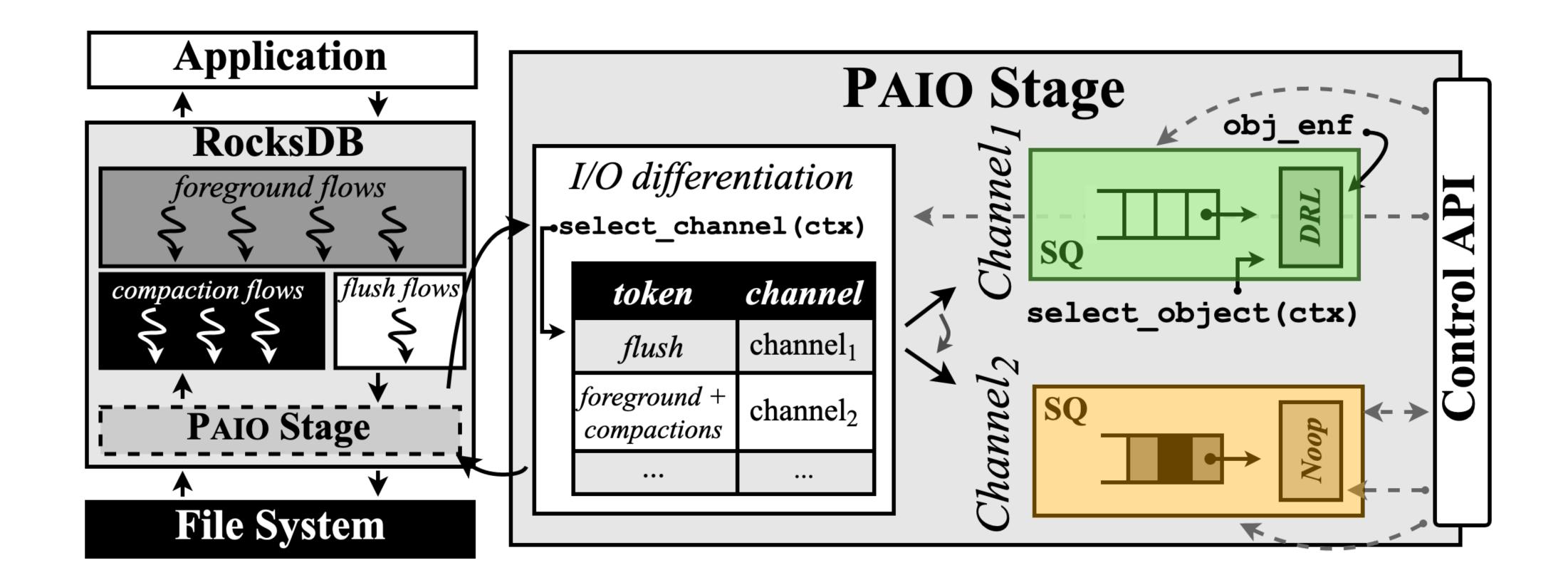


I/O differentiation





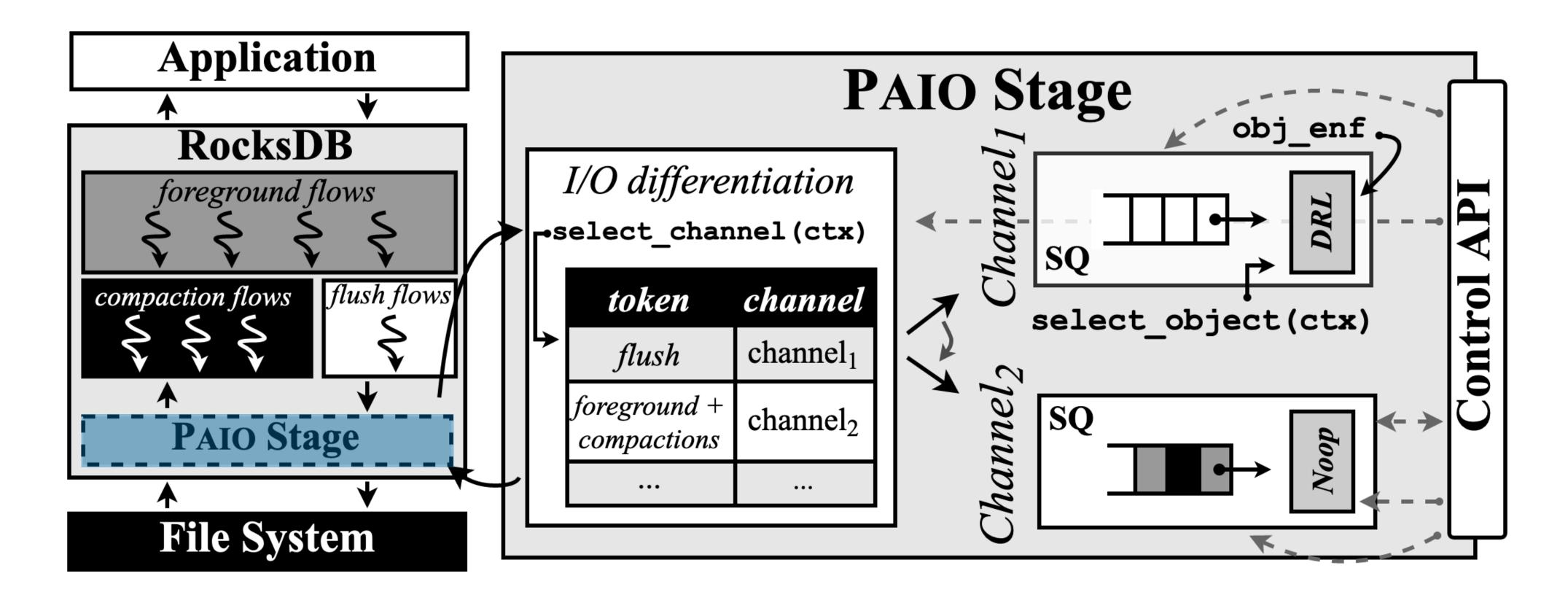
I/O enforcement



PAIO currently supports **Noop** (passthrough) and **DRL** (token-bucket) enforcement objects



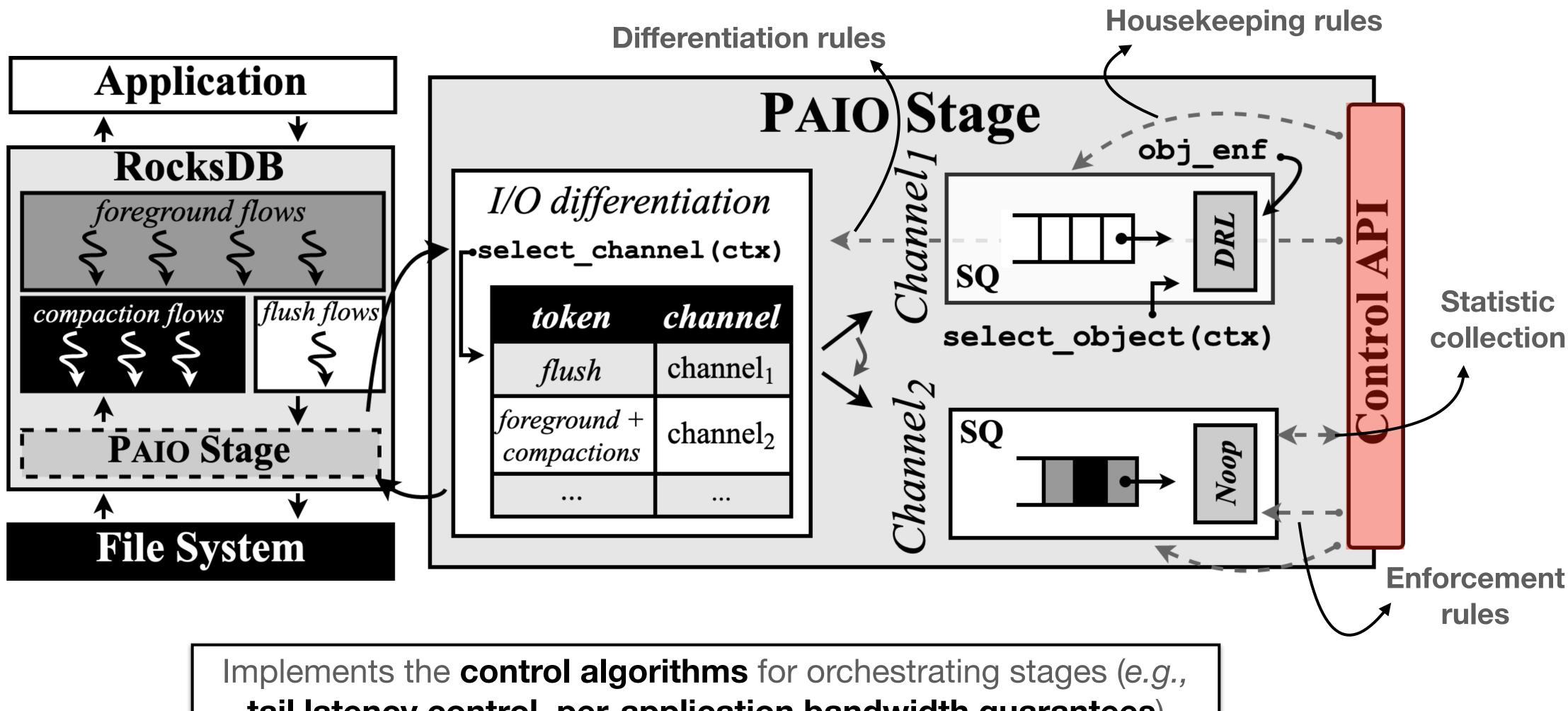
I/O enforcement



Requests return to their original I/O path



Control plane interaction



tail latency control, per-application bandwidth guarantees)

Statistic



Tail latency control in LSM-based KVS

RocksDB

- Latency spikes occur due to L₀-L₁ compactions and flushes being slow or on hold
- Interference between foreground and background tasks generates high latency spikes

SILK

- I/O scheduler
 - Allocates bandwidth for internal operations when client load is low
 - Prioritizes flushes and low level compactions
 - Preempts high level compactions with low level ones
- Required changing several core modules made of thousands of LoC

PAIO

- Stage provides the I/O mechanisms for prioritizing and rate limiting background flows • Integrating PAIO in RocksDB only required adding 85 LoC Control plane provides a SILK-based I/O scheduling algorithm



Tail latency control in LSM-based KVS

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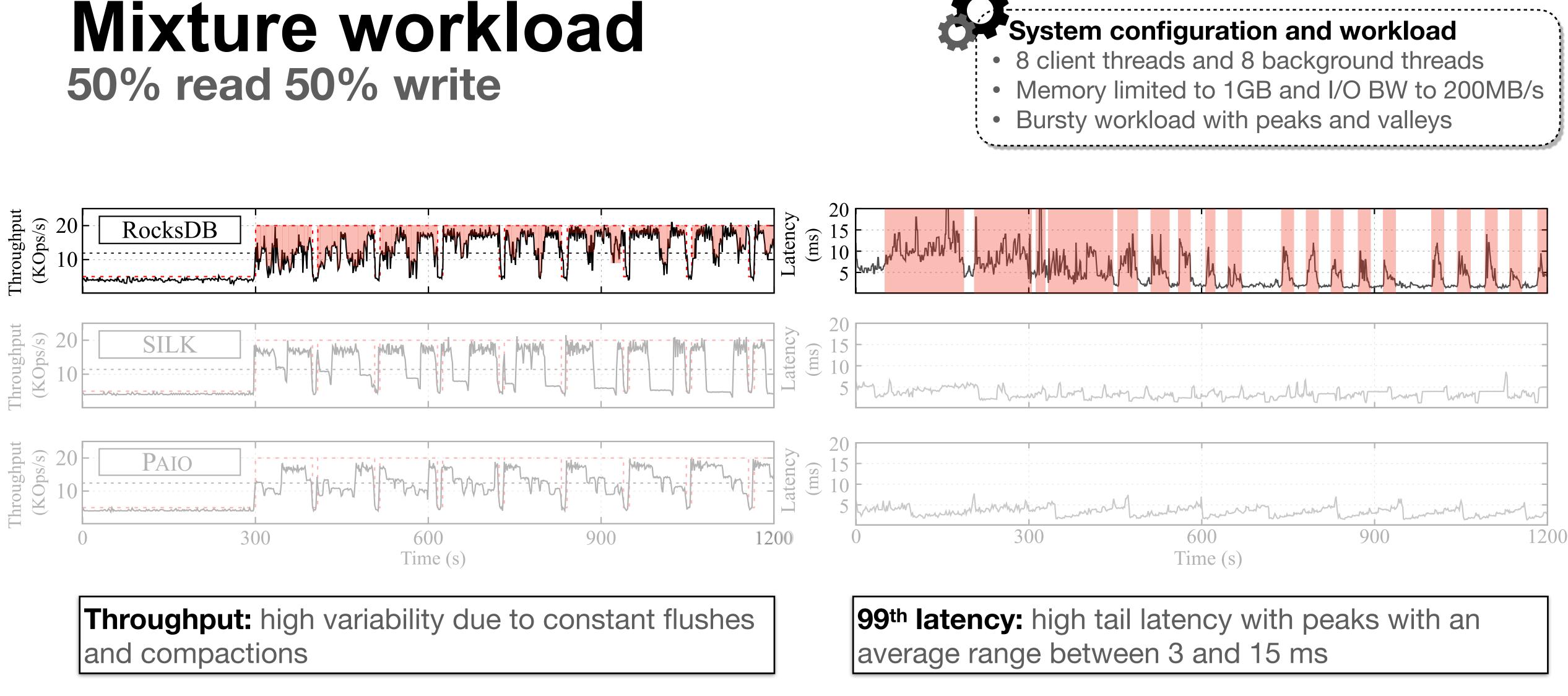
PAIO

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- Control plane provides a SILK-based I/O scheduling algorithm

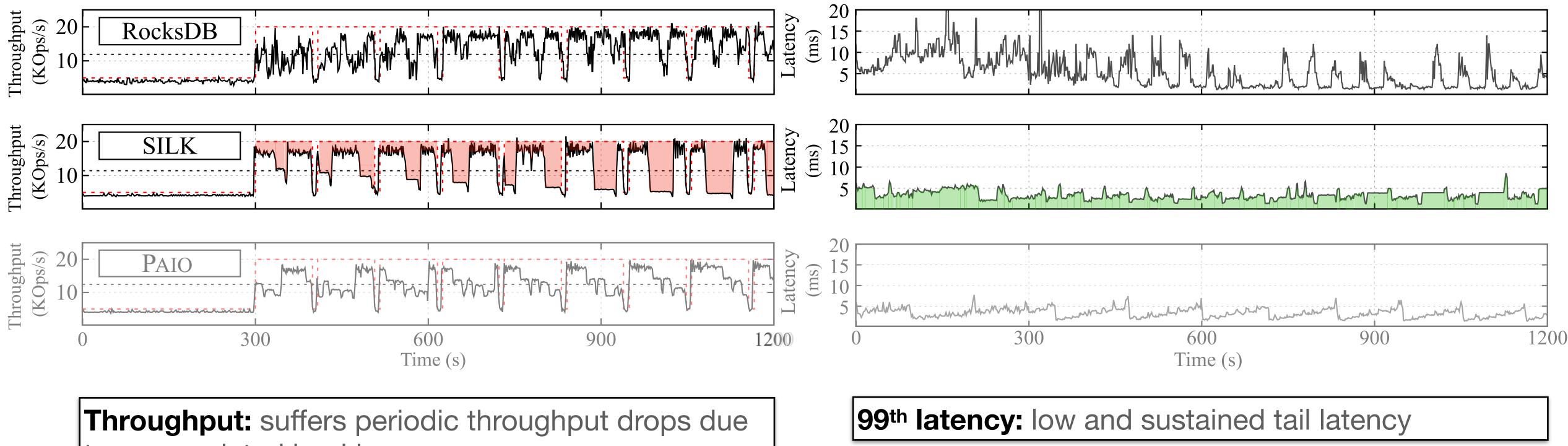
Note: By propagating application-level information to the stage, PAIO can enable similar control and performance as systemspecific optimizations







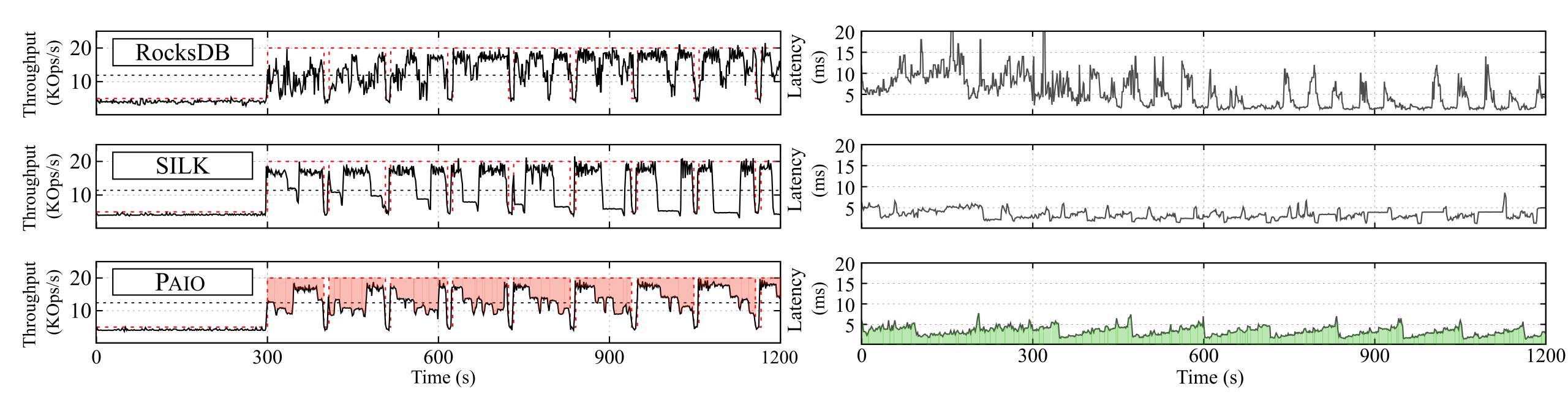
Mixture workload 50% read 50% write



to accumulated backlog



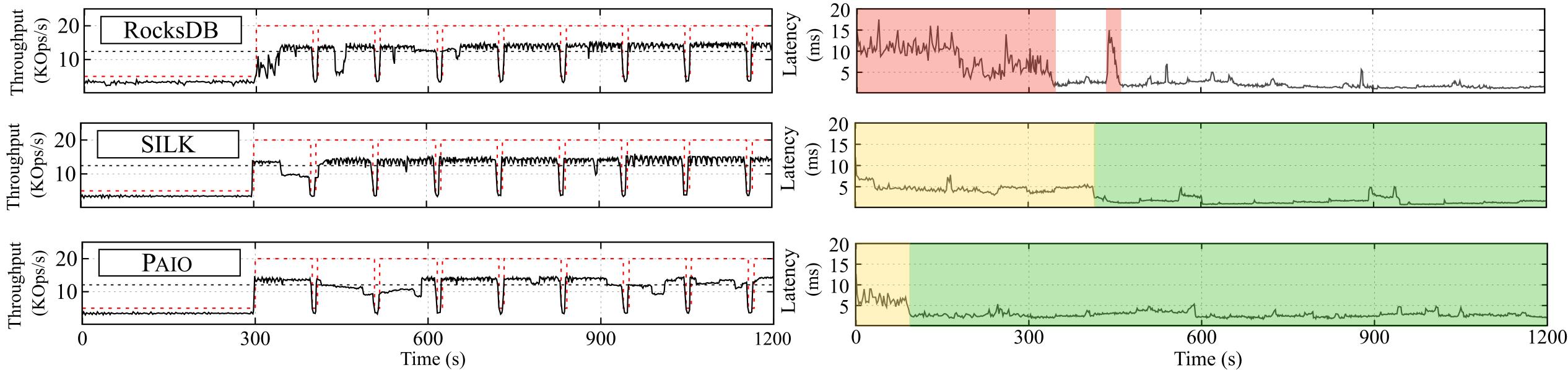
Mixture workload 50% read 50% write



PAIO and SILK observe a 4x decrease in absolute tail latency



Read-heavy workload 90% read 10% write



Sustained tail latency but higher than SILK, due to not preempting compactions

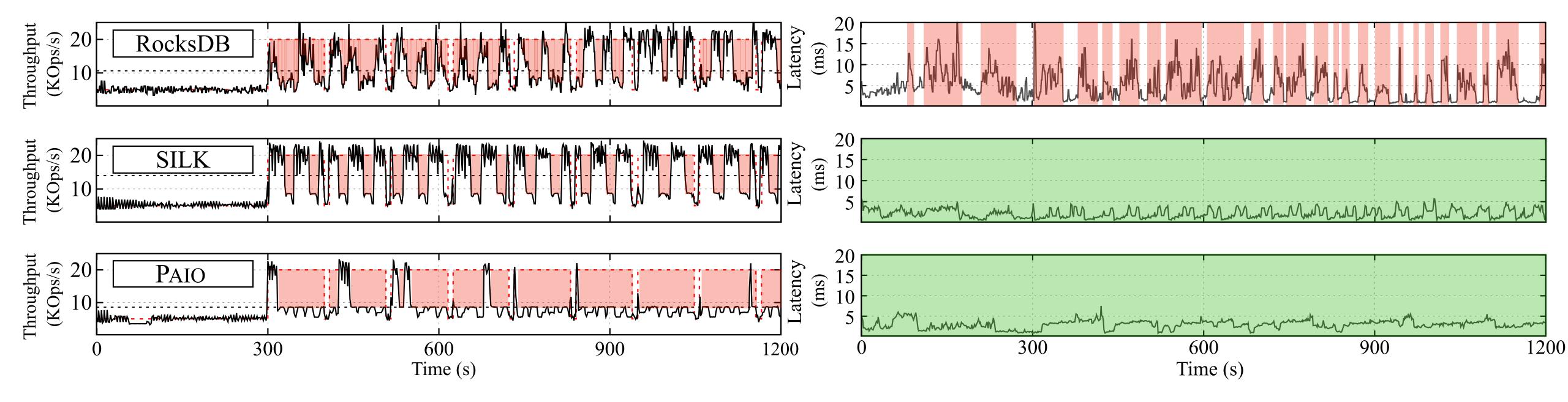


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Write-heavy workload **10% read 90% write**



Since flushes occur more frequently, PAIO slows down high level compactions more aggressively, temporarily halting low level ones



Summary

PAIO, a user-level framework that enables system designers to build *custom-made* data plane stages

Decouples system-specific optimizations to dedicated I/O layers

Data plane stages

- Tail latency control in LSM-based KVS (RocksDB)

Enables similar control and I/O performance as system-specific optimizations

Combines ideas from Software-Defined Storage and context propagation

Per-application bandwidth control in shared storage settings (TensorFlow)





PAIO: General, Portable I/O **Optimizations** with Minor Application Modifications

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 Cat Public CaT: Content-aware Tracing and Analysis for Distributed Systems Python 3 	Image: bdus Public Forked from albertofaria/bdus A framework for implementing Block Devices in User Space C
 prisma Public A data prefetching storage data plane for accelerating DL training performance. C++ \$\$ 5 	

